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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS:

Walter Fix et al.

SERIAL NO:

10/541,957

FILED:

July 8, 2005

EXAMINER:

Not assigned

ART UNIT:

2832

FOR:

ORGANIC FIELD EFFECT TRANSISTOR AND INTEGRATED

CIRCUIT

ATTY DKT NO .:

411000-138

CUSTOMER NO.: 27162

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

Pursuant to 37 C.F.R. §1.56(a), Applicant(s) hereby cite(s) the enclosed documents listed on the attached copy of Form PTO-1449 which are believed to be material to the patentability of the above-identified application.

This Information Disclosure Statement is filed in accordance with the paragraph of 37 CFR §1.97 indicated below:

X §1.97(b)

This Information Disclosure Statement is filed:

- (1) Within three months of the filing date of a national application; OR
- Within three months of the date of entry of the national (2) stage of an international application; OR
- Before the mailing of a first Office Action on the merits. (3) No fee or statement is required.
- §1.97(c) This Information Disclosure Statement is filed after the period specified in paragraph (b) above, but before the mailing date of either:

	(1) (2)	A Final Action or under 37 CFR §1.113; OR A Notice of Allowance under 37 CFR §1.311; AND
	is acc	ompanied by either: (check one) The statement as specified in 37 CFR §1.97(e) set our below; OR The fee of \$180.00 under 37 CFR §1.17(p).
§1.97(d)	This Informa of eith	tion Disclosure Statement is filed after the mailing date er:
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§1.97(e)	The undersig	ned Attorney hereby states that:
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The relevancy of the foreign language documents listed on the attached PTO 1449 form is that these documents were cited in one or more of the commonly owned copending applications cited in the Disclosure Statement accompanying this paper or are cited in one or more of the commonly owned patents in the attached PTO 1449 or

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are foreign published documents of one or more of the applicants named in the accompanying Disclosure Statement and cited in the attached PTO 1449 or were cited in a pending commonly owned foreign patent application, all having subject matter generally related to the subject matter of the instant application noted above as explained in the accompanying Disclosure Statement. The Examiner is respectfully requested to consider and make of record all of the cited documents.

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

FIRST CLASS CERTIFICATE

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Respectfully submitted,

Walter Fix et al.

by William Squire, Reg. No. 25,378

Attorney for Applicants

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9	nd to a collection of information unless it d	isplays a valid OMB control number	
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	Application Number	10/541,957	
	Filing Date	July 8, 2005	
	First Named Inventor	Walter Fix	
	Group Art Unit	2832	
	Examiner	Not assigned	
	Attorney Docket Number	411000-138	

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	STATEMENT BY APPLICANT			First Named Inventor	Walter Fix
				Group Art Unit	2832
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Application Number	MASTER LIST III – includes all to 8/4/05 for file 124			
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	STATEMENT BY APPLICANT			Filing Date		
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	STATEMENT E	BY APPLICA	ANT	Filing Date	
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. BOX 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED

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APPLICANTS:

Walter Fix et al.

SERIAL NO:

10/541,957

FILED:

July 8, 2005

EXAMINER

Not assigned

ART UNIT

2832

FOR:

ORGANIC FIELD EFFECT TRANSISTOR AND INTEGRATED

CIRCUIT

ATTY DKT NO .:

411000-138

CUSTOMER NO.: 27162

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

DISCLOSURE STATEMENT UNDER 37 CFR 1.56

SIR:

This paper is to bring to the attention of the PTO the following commonly owned copending U.S. applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like. Many of these applications also have one or more common inventors. The enclosed PTO 1449 lists these applications. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed on the attached PTO 1449.

Application No.	<u>Title</u>	Inventors	Atty. Dkt. No.
10/344,951	Organic Field-Effect Transistor (OFET), A Production Method Therefor, An Integrated Circuit Constructed From the Same and Their Uses	Adolf Bernds et al.	411000-99
10/362,932	Organic Field Effect Transistor, Method for Structuring an OFET and Integrated Circuit	Adolf Bernds et al	411000-110

10/380,113	Organic Rectifier, Circuit, RFID Tag and Use of an Organic Rectifier	Adolf Bernds et al.	411000-106
10/381,032	Electrode and/or Conductor Track for Organic Components and Production Method Thereof	Adolf Bernds et al.	411000-105
10/433,959	Organic Field Effect Transistor, Method For Structuring an OFET and Integrated Circuit	Adolf Bernds	411000-108
10/433,961	Device For Detecting and/or Transmitting at Least One Environmental Influence, Method for Producing Said Device and Use Thereof	Wolfgang Clemens et al.	411000-111
10/467,636	Organic Field Effect Transistor With a Photostructured Gate Dielectric, Method for the Production and Use Thereof in Organic Electronics	Adolf Bernds et al.	411000-104
10/473,050	Device With At Least Two Organic Electronic Components and Method for Producing the Same	Adolf Bernds et al.	411000-113
10/479,234	Organic Field Effect Transistor, Method for Production and Use Thereof in the Assembly of Integrated Circuits	Adolf Bernds et al.	411000-101
10/479,238	Method For Producing Conductive Structures by Means of Printing Technique, and Active Components Produced Therefrom For Integrated Circuits	Adolf Bernds et al.	411000-100
10/492,922	Insulator for An Organic Electronic Component	Erwann Guillet et al.	411000-115
10/492,923	Electronic Unit, Circuit Design for the Same and Production Method	Wolfgang Clemens et al.	411000-114
10/498,610	Organic Field Effect Transistor with Offset Threshold Voltage and the Use Thereof	Walter Fix et al.	411000-119
10/508,640	Logic Component Comprising Organic Field Effect Transistors	Walter Fix et al.	411000-120
10/508,737	Device and Method for Laser Structuring Functional Polymers and	Adolf Bernds et al.	411000-121
10/517,750	Substrate for an Organic Field Effect Transistor, Use of the Substrate, Method of Increasing the Charge Carrier Mobility and Organic Field Effect Transistor (OFET)	Wolfgang Clemens et al.	411000-122
10/523,216	Electronic Component Comprising Predominantly Organic Functional Materials And A Process For The Production Thereof	Adolf Bernds et al.	411000-123
10/523,487	Electronic Device	Wolfgang Clemens et al.	411000-124
10/524,646	Organic Component for Overvoltage Protection and Associated Circuit	Walter Fix et al.	411000-127

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10/533,756	Organic Electronic Component with High- Resolution Structuring and Process for the Production Thereof	Wolfgang Clemens et al.	411000-128
10/534,678	Measuring Apparatus for Determining an Analyte in a Liquid Sample	Wolfgang Clemens et al.	411000-129
10/535,448	Organic Electronic Component Comprising Semi-Conductive Functional Layer and Method for Producing Said Component	Wolfgang Clemens et al.	411000-131
10/535,449	Organic Electronic Component Comprising the Same Organic Material for at Least Two Functional Layers	Adolf Bernds et al.	411000-132
10/344,926	An Electronic Circuit Having an Encapsulated Organic-Electronic Component, and a Method for Making an Encapsulated Organic-Electronic Component	Wolfgang Clemens et al.	411000-133
10/541,815	Organo-Resistive Memory Unit	Axel Gerlt et al.	411000-136
10/541,956	Board or Substrate for an Organic Electronic Device and Use Thereof	Wolfgang Clemens et al.	411000-137
10/541,957	Organic Field Effect Transistor And Integrated Circuit	Walter Fix et al.	411000-138
10/543,561	Organic Storage Component and Corresponding Triggering Circuit	Wolfgang Clemens et al.	411000-139
10/542,678	Organic Electronic Component and Method For Producing Organic Electronic Devices	Adolf Bernds et al.	411000-140
10/542,679	Use of Conductive Carbon Black/Graphite Mixtures for the Production of Low-Cost Electronics	Adolf Bernds et al.	411000-141

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Nov. 2, 2005

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Respectfully submitted,

Walter Eix et al.

by William Squire, Reg. No. 25,378

Attorney for applicants CARELLA, BYRNE, BAIN, GILFILLAN, **CECCHI, STEWART & OLSTEIN**

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Substitute f	Substitute for form 1449A/PTO			Complete if Known		
				Application Number	10/541,957	
	INFORMATION	I DISCLOSU	IRE	Filing Date	July 8, 2005	
	STATEMENT I	BY APPLICA	NT	First Named Inventor	Walter Fix	
				Group Art Unit	2832	
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			U.S. PATENT DOC	UMENTS		
Examiner Initial*	Cite No.1	Document Number Number-Kid Code ^{2 (if known)}	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
					See accompanying Disclosure Statement filed herewith	
	133	US-10/344,926	02/12/2004	Adolf Bernds et al.		
	99	US-10/344,951	02/12/2004	Adolf Bernds et al.		
	110	US-10./362,932	10/02/2003	Adolf Bernds et al.		
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•	108	US-10/433,959	04/01/2004	Adolf Bernds et al.		
· · · · · · · · · · · · · · · · · · ·	111	US-10/433,961	04/01/2004	Wolfgang Clemens et al.		
	109	US-10/451,108	05/13/2004	Mark Giles et al.		
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	101	US-10/479,234	12/30/2004	Adolf Bernds et al.		
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	123	US-10/523,216	N/A	Adolf Bernds et al.		
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				Application Number	10/541,957
INFORMATION DISCLOSURE			RE	Filing Date	July 8, 2005
	STATEMENT BY APPLICANT			First Named Inventor	Walter Fix
				Group Art Unit	2832
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129	US-10/534,678	N/A	Wolfgang Clemens et al.	
131	10/535,448	N/A	Wolfgang Clemens et al.	
132	10/535,449	N/A	Walter Fix et al.	
136	US-10/541,815	N/A	Axel Gerlt et al.	
137	US-10/541,956	N/A	Wolfgang Clemens et al.	
138	US10/541,957	N/A	Walter Fix et al.	
139	US-10/543,561	N/A	Wolfgang Clemens et al.	
140	US-10/542,678	N/A	Adolf Bernds et al.	
141	US-10/542,679	N/A	Adolf Bernds et al.	
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